

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:	)	
	)	
Possley	)	Examiner: not yet assigned
	)	
Application No.: not yet assigned	)	Art Unit: not yet assigned
	)	
Filed: not yet assigned	)	
	)	
For: <u>GATE ARRAY ARCHITECTURE</u>	)	

**PRELIMINARY AMENDMENT**

ASSISTANT COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

Dear Sir:

The above-referenced patent application is a continuation patent application under 37 CFR 1.53(b). Applicant respectfully requests that the Examiner enter the following amendments and consider the following remarks. A "Marked Version to Show Changes" is attached hereto.

**IN THE SPECIFICATION:**

Please insert the following new paragraph in the front of the patent application:

This is a Continuation Patent Application of U.S. Patent Application Serial No. 09/262,458, filed March 4, 1999, titled "Gate Array Architecture," by Brian D. Possley, assigned to the assignee of the present application and herein incorporated by reference.

**IN THE CLAIMS:**

Please cancel claims 1-12, 21-26.

**REMARKS**

The above-referenced patent application is a continuation patent application of U.S. patent application serial no. 09/262,458, filed on March 4, 1999. In the aforementioned parent application, claims 13-20 had been restricted by the Examiner. Therefore, these claims shall be prosecuted in this continuation. No prosecution history estoppel results from any of these actions because it merely permits these claims to be prosecuted separately. No claimed subject matter of any kind has been surrendered by these actions.

**CONCLUSION**

In view of the foregoing, it is respectfully asserted that the claims pending in this application, as amended, are in condition for allowance. If the Examiner has any questions, he is invited to contact the undersigned at (503) 264-0967. Consideration of this patent application and early allowance of these claims is respectfully requested.

Respectfully submitted,

Dated:

7/10/00



Howard A. Skaist  
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**MARKED VERSION TO SHOW CHANGES**

**IN THE SPEC:**

Insert the following paragraph at page 2, line 4:

This is a Continuation Patent Application of U.S. Patent Application Serial No. 09/262,458, filed March 4, 1999, titled "Gate Array Architecture," by Brian D. Possley, assigned to the assignee of the present application and herein incorporated by reference.

**IN THE CLAIMS:**

Cancel claims 1-12, 21-26.

09/262,458